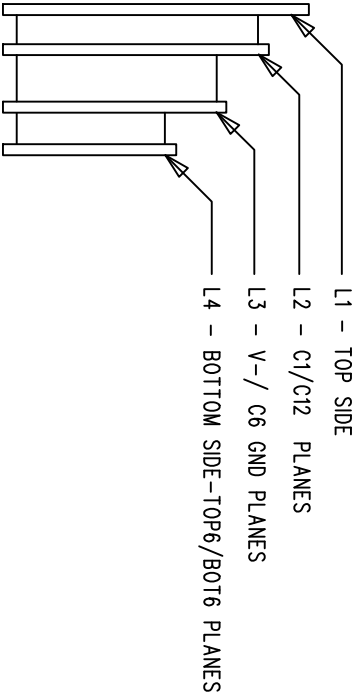
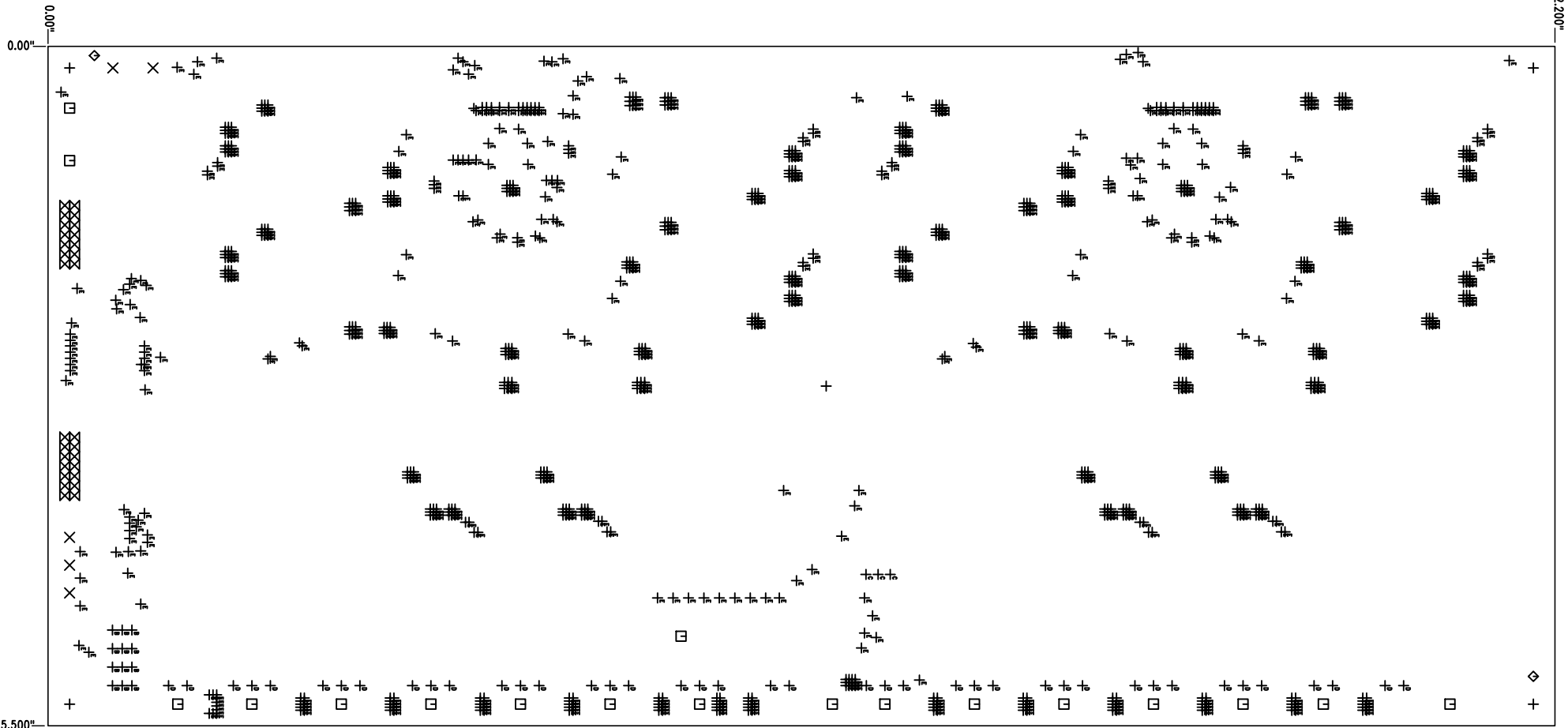
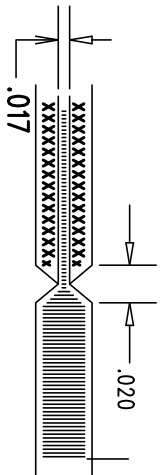


REVISION HISTORY			
ECO#	REV	DESCRIPTION	DATE
-	1	PRODUCTION FAB	JIM D. 1-11-13



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 2 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003"
 - IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 - GOLD IMMERSION BOTH SIDES.
 - (LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE ROHS COMPLIANT.
- DO NOT ALTER SOLDER MASK MAINTAIN .0018" OVERSIZE ON SMT PADS. A .005" WEBBING IS REQUIRED BETWEEN SMD PADS.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U7.
U7 SOLDER MASK IS 25 MIL AND PAD SIZE IS 29 MIL.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.
- SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"



SIZE	QTY	SYM	PLATED	TOL
0.19	5	+	YES	+/- .003"
0.063	5	X	YES	+/- .003"
0.094	18	□	YES	+/- .003"
0.07	2	◇	NO	+/- .003"
0.035	28	Σ	YES	+/- .003"
0.031	12	⊞	YES	+/- .003"
0.0437	3	⊞	YES	+/- .003"
0.05512	41	⊞	YES	+/- .003"
0.012	1009	⊞	YES	+/- .003"

UNLESS OTHERWISE SPECIFIED			
DIMENSIONS ARE IN INCHES			
TOLERANCES: FRACTIONS DECIMALS			
0.XXX" = 10.001" 51"			
INTERPRET DIM AND TOL PER ASME Y14.5M-1994			
THIRD ANGLE PROJECTION			
DO NOT SCALE DRAWING			
SCALE: NONE			
FILENAME: DC2064A-1P08			
SHT 1 of 1			

APPROVALS		TITILE: FABRICATION DRAWING:	
PCB DES.	NC	HIGH EFFICIENCY BIDIRECTIONAL	
APP ENG.	JIM D.	MULTICELL BATTERY BALANCER	
SIZE	N/A	IC NO LTC3300LXE-1/LTC6803IG-2 REV.	
DATE	N/A	DEMO CIRCUIT 2064A	
REV	1		

